

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

OIR NOV 26 2001 JC6
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
In re U.S. Patent Application of)
HAYASHI et al.)
Application Number: 09/976,052)
Filed: October 15, 2001)
For: LEVEL SHIFT CIRCUIT AND SEMICONDUCTOR)
INTEGRATED CIRCUIT)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

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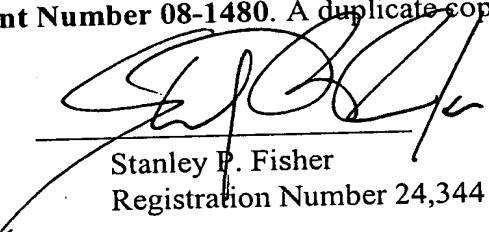
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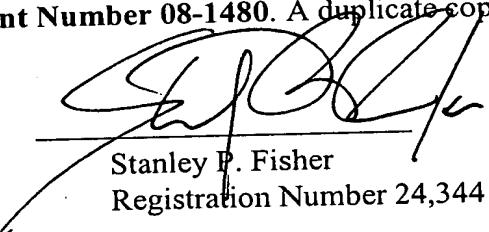
Sir:

The below-identified communications are submitted in the above-captioned application or proceeding:

<input checked="" type="checkbox"/>	Priority Documents (f)	<input type="checkbox"/> Verified English Translation
<input checked="" type="checkbox"/>	Notice of Priority	<input type="checkbox"/> Information Disclosure Statement
<input type="checkbox"/>	Response to Missing Parts with executed declaration	<input type="checkbox"/> Notice of Related Applications
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The Commissioner is hereby authorized to charge payment of any fees associated with this communication, including fees under 37 C.F.R. § 1.16 and 1.17 or credit any overpayment to Deposit Account Number 08-1480. A duplicate copy of this sheet is attached.


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November 26, 2001

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